

## Why Composable Infrastructure?

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## **Forward Looking Statements**

### This presentation contains forward looking statements

- An intelligent & educated exposition of a new approach to compute, networking & storage technology
- For any third party technologies mentioned here, these are our interpretations of those technologies
- We assume you understand a little about how compute, networking & storage are currently bolted together



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50,000 IT end users & storage pros worldwide



What

We

**Educate** vendors and users on cloud storage, data services and orchestration



## Support & promote

business models and architectures: OpenStack, Software Defined Storage, Kubernetes, Object Storage



Understand Hyperscaler requirements Incorporate them into standards and programs





- What's driving current compute, network & storage developments?
- What is Composable Infrastructure?
- What steps are we taking to realize it?

## What is an Application



#### Task

- Apps need a system
  - Has requirements
    - CPU cores
    - Memory size
    - Network BW
    - Network Location
    - Availability

#### Store

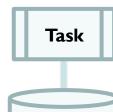
- Most apps need a persistent store
  - Has requirements
    - BW
    - Latency
    - Capacity
    - Availability

#### Examples

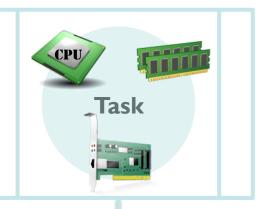
- RDBMS
- Web Servers
- ML application

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### Application



Store





## Keeping up with the Apps

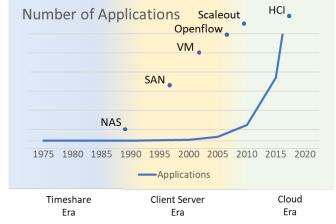
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### Decades of history - Timeshare to Client Server to Cloud

- Applications growing geometrically
  - Faster than technology
- Causes an ever growing need for more HW
- Growing configuration requirements
- Growing management problem

### Flexibility and cost drive the direction

- Overprovisioning reductions
  - > Reduce costs
- Disaggregation focuses management problem



## Keeping up with the Apps

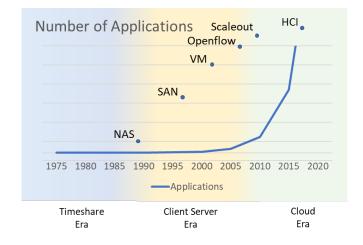
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### Timeshare to client server

- How many cheap servers = one mainframe
  - > Unit of allocation decreases
- Flexibility
  - > Management
  - > Configuration
  - > Availability

### Client Server to Cloud

- Hitting limits of scale up
- Elasticity without infrastructure investment
- Location abstraction
- Externally: Unit of allocation drops to App
  - > But within the cloud the same problems



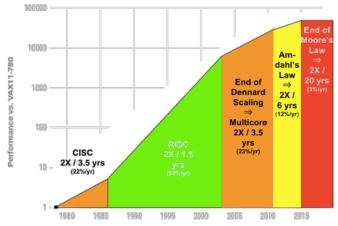
## While Applications are GROWING

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- Dennard's scaling ended
  - Power leakage and heat prevent cycle scaling
- Multicore hit Amdahl's law
  - Applications can only be parallelized so far
- Moore's law is ending
  - Physical size limitations
- What's left then?
  - Domain Specific Architectures
    - > Graphics Processing Unit (GPU)
    - > Offloading Network Interface Controllers (NIC)
    - > Tensor Processing Unit (TPU)
    - > FPGA Based Accelerators
  - This increases configuration complexity

#### David Patterson's presentation at ISSCC2018 https://youtu.be/NZS2TtWcutc

#### 40 years of Processor Performance



Based on SPECintCPU. Source: John Hennessy and David Patterson, Computer Architecture: A Quantitative Approach, 6/e 2018

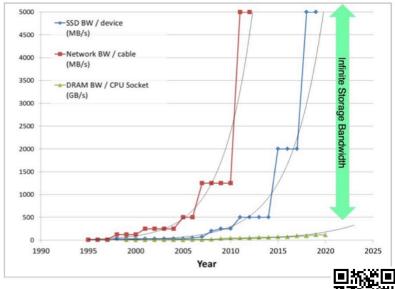


## **Multicore Problems**



- Thanks to discussions started by Fritz Kruger and Allen Samuels
- Storage and Network throughput growth outstripping CPU
  - By 2020 only a couple SSDs per socket will be needed to outstrip ability to shuttle data in and out
    - > SCM is going to make this worse
- This suggests that larger multicore systems will compete for DMA bandwidth
  - Fewer apps per server
  - Age of Hyper Converged Infrastructure may be closing

- Network, Storage and DRAM Trends
  - DRAM throughput is a proxy to CPU capability
  - Storage Bandwidth is not literally infinite
  - But the ratio of Network and Storage to CPU throughput is widening very quickly



CPU Bandwidth – The Worrisome 2020 Trend, Fritz Kruger, Mar 2016



# **Today's Applications**

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#### Task

- Many System options
  - > CPUs/SoCs
  - > Core counts
  - > DDR Capacity
  - > NICs
  - > Accelerators
  - > GPUs/TPUs

#### Store

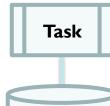
- Many Store options
  - > Cold stores
  - > HDDs
  - > SSDs
  - > Persistent memory (SCM) devices

#### All must go into a box

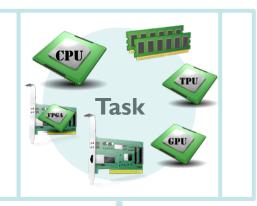
- Dictated by App requirements
- What and how much decided at purchase time
- No going back, no evolution

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### Application



**Store** 





## What is the Problem

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#### Application requirements wide and varied

Complicated set of hardware requirements

#### Must map these requirements onto physical hardware

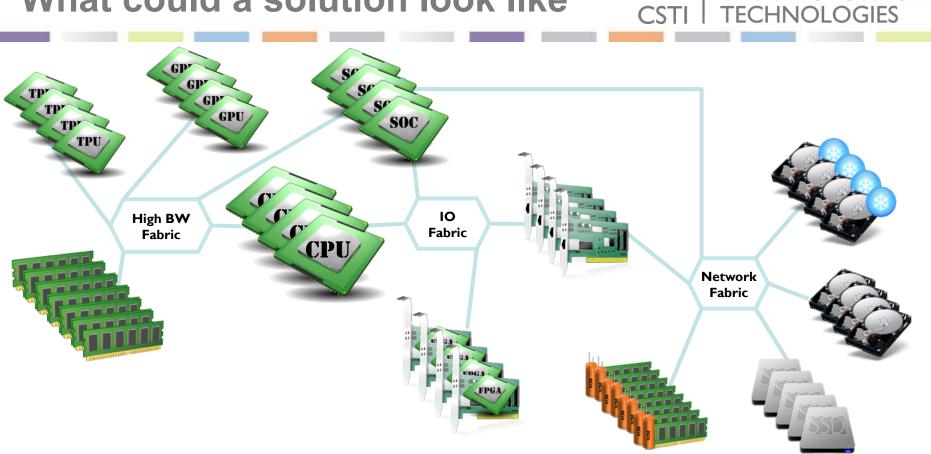
- Due to core counts, multiple apps must be mapped to single system
- Forces IT managers to be system designers
- Forces overprovisioning inside the system
- Availability and Competition issues

#### Application requirements quickly and constantly evolve

- Mapping occurs at purchase time and cannot evolve
- Invalidates system design requirements
- Growth rate of apps
  - Forces overprovisioning system counts for elasticity
- Ever growing classes of hardware systems
  - Lifecycle management (scaling, EOL, etc) becomes a multi-vectored problem

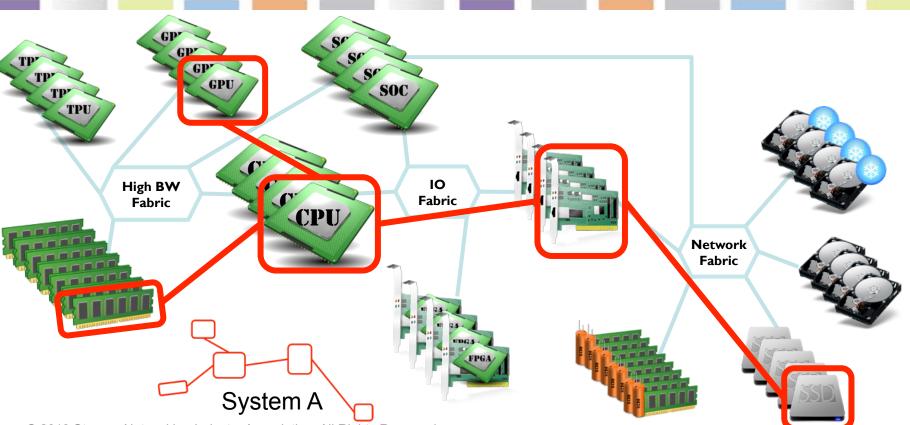
#### The multicore server as the unit of app allocation is now too big and complicated

## What could a solution look like



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## What could a solution look like



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# We are already moving this way

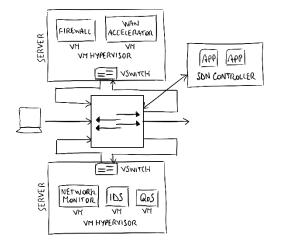
### Software Defined Networking

Virtual networks created dynamically

### Software Defined Storage

- Disaggregation exists and broadening
  - > NAS, SAN
  - > Scale out
  - > NVMeoF
- Fabric Attached Persistent Memories
- Storage Accelerators
  - SoC and FPGA based

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- What's driving current compute, network & storage developments?
- What is Composable Infrastructure?
- What steps are we taking to realize it?

What is Composable Infrastructure?

- Compose to form by putting together
- Infrastructure the underlying foundation or basic framework (as of a system or organization)



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Source: Merriam Webster dictionary

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## What is Composable Infrastructure?

- Composable infrastructure treats compute, storage, and network devices as pools of resources that can be provisioned as needed, depending on what different workloads require for optimum performance."<sup>1</sup>
- \* "A composable infrastructure is a framework whose physical compute, storage and network fabric resources are treated as services. In a composable infrastructure, resources are logically pooled so that administrators don't have to physically configure hardware to support a specific software application. Instead, the software's developer defines the application's requirements for physical infrastructure using policies and service profiles and then the software uses application programming interface (API) calls to create (compose) the infrastructure it needs to run on bare metal, as a virtual machine (VM) or as a container."<sup>2</sup>
- Composable infrastructure brings together compute, storage and network fabric into one platform, similar to a converged or hyperconverged infrastructure. It also integrates a software-defined intelligence and a unified API to "compose" these fluid resource pools."<sup>3</sup>
  - 1. <u>https://www.networkworld.com/article/3266106/data-center/what-is-composable-infrastructure.html</u> 2. <u>https://www.zdnet.com/article/composable-infrastructure/</u>

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3. https://www.itprotoday.com/business-resources/just-what-heck-composable-infrastructure-anyway

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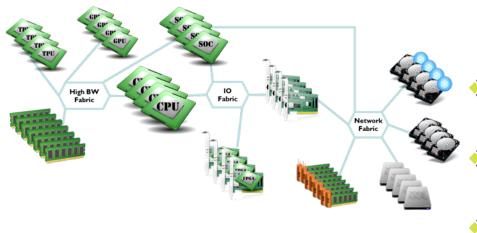
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3. https://www.itprotoday.com/business-resources/just-what-heck-composable-infrastructure-anyway

- Separate compute, storage, networking components
- Pools of resources
- ✓ Don't need to be physically proximate
- Compose as needed via orchestration
- Scalability is not one way
- API driven (autonomous operation)
- Driven by application needs

## Disaggregation





- Disaggregate to separate into component parts (Merriam Webster)
- Systems server, storage device, network switch
- Components CPU, memory, discrete disks, PCI devices
- Anything that can be accessed on a network fabric

## But what about virtualization/ containerization?

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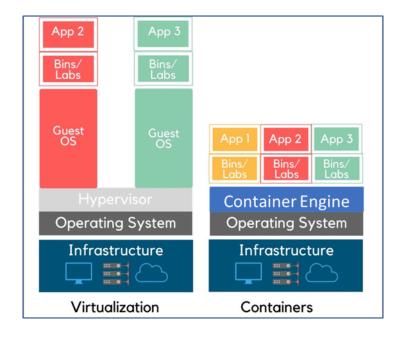
### Fundamentally the opposite!

### Virtualization

- Take one instance of a resource (e.g., a server) and slice it up
- Optimal use of a single resource by sharing it among multiple apps
- Each app isolated above the kernel level through the use of VMs

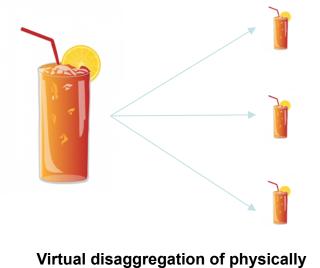
### Containerization

- OS level virtualization method
- Multiple apps share the same kernel without having to launch VMs



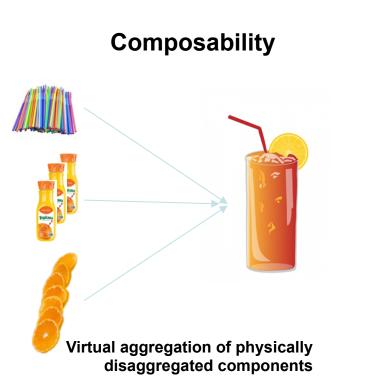
## Virtualization & Containerization vs. Composability

#### Virtualization/Containerization



#### Virtual disaggregation of physically aggregated components

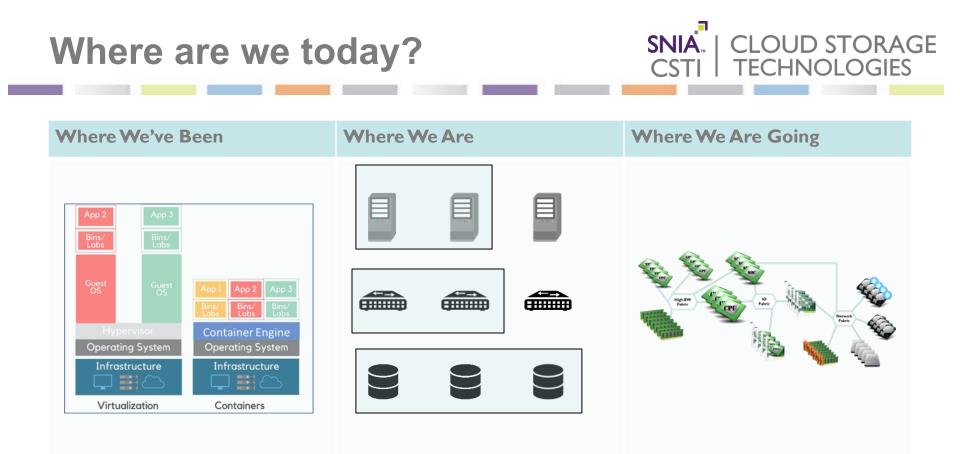
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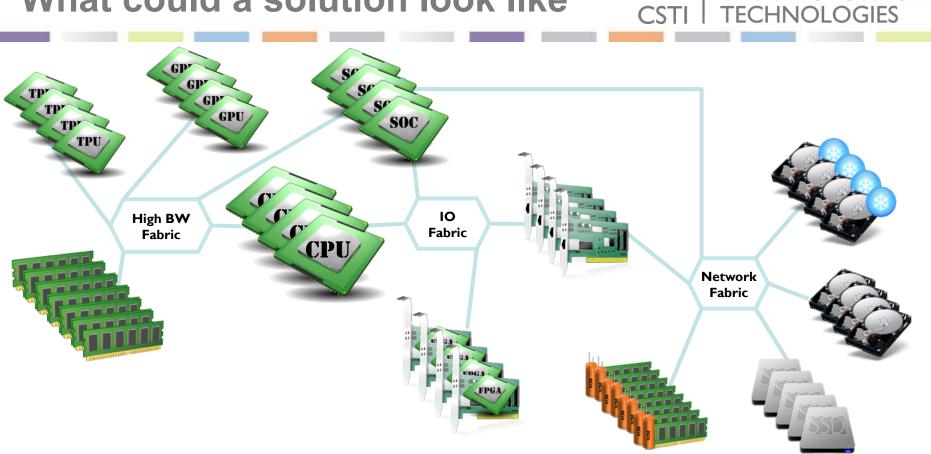
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- What's driving current compute, network & storage developments?
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## What could a solution look like



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- Peripheral Component Interconnect Express (PCIe)
- Standard Processor Device Interconnect
  - Extremely Broad support
- Once only a local interconnect
- Switches enabled creating a fabric
- Still Target/Initiator architecture
- Version 4 emerging, 5 on its heels





### Non-Volatile Memory express (NVMe)

- Storage access protocol
  - > Redesign due to high speeds flash and memory devices
- Default interconnect is PCIe

### Non-Volatile Memory express over Fabrics (NVMe-oF)

- Disaggregates from local fabrics
- Permits the use of general switched and routed fabrics
  - > RDMA
    - Infini-Band (IB)
    - RoCE v2
    - iWarp
  - > Fibre Channel (FC and FCoE)
  - > TCP



- Cache Coherent Interconnect for Accelerators (CCIX), pronounced "see-six"
- Key Members: AMD, ARM, Huawei, Mellanox, Qualcomm, Xilinx
- CCIX is an open cache coherent interconnect architecture developed by the CCIX Consortium.
  - CCIX is designed to simplify the communication between the central processor and the various accelerators in the system through a cache-coherent extension to standard PCIe.



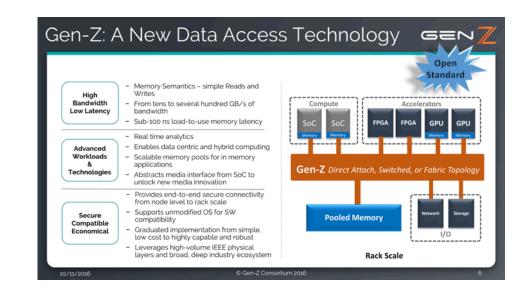


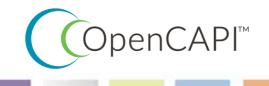
### Founder Members

 AMD, ARM, Broadcom, Cray, Dell EMC, Hewlett Packard Enterprise, Huawei, IDT, Mellanox, Micron, Microsemi, Samsung, SK Hynix, and Xilinx

### Gen Z is

 An open systems Interconnect designed to provide memorysemantic access to data and devices via direct-attached, switched or fabric topologies



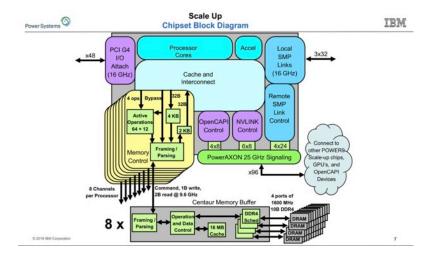


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- Open Coherent Accelerator Processor Interface
- Strategic Members:
  - AMD, Google, IBM, Mellanox, Micron, NVIDIA, Western Digital, Xilinx

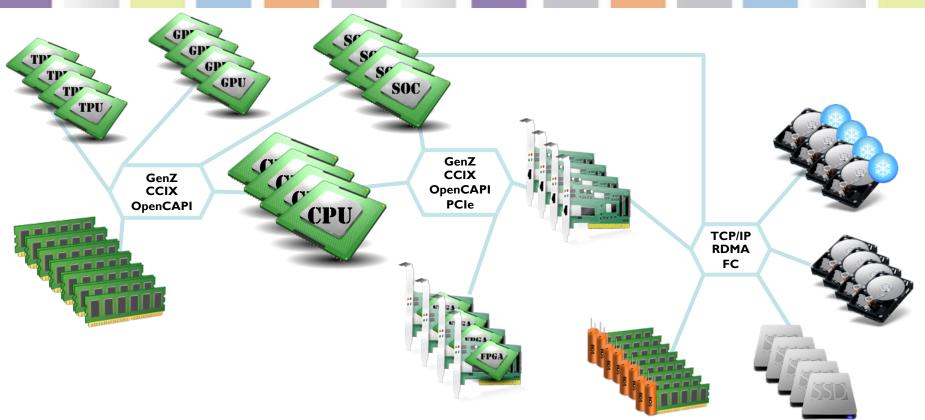
### OpenCAPI is

- An Open Interface Architecture that allows any microprocessor to attach to it
- Provides
  - Coherent user-level accelerators and I/O devices
  - Advanced memories accessible via read/write or user-level DMA semantics
- Intended to be agnostic to processor architecture



## What could a solution look like

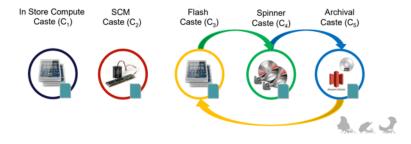
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## **Storage Disaggregation**

### New Ideas

- Eusocial storage devices
  - Fully autonomous, disaggregated and composable storage devices



A CPU cycles and DDR bandwidth. In this article, we examine a new interface to storage devices that can leverage existing and new CPU and DRAM resources to take over data management tasks like availability recovery, and migrations. This new interface provides a roadmap for devia to-device interactions and more powerful storage devices capable of provid ing in-store compute services that can dramatically improve perfor We call such storage devices "eusocial" because we are inspired by eusocial insects like ants, termites, and bees, which as individuals are primitive but collectively accomplish amazing thing The Evolution of the Problem Why Try Smart Storage Again, and Why Now? sense at a time when processor cycles were incredibly scarce and costly. However, over th ping deduplication, compaction, sorting, scrubi ecture With each compute the need for additional compute and memory in the device pushing up per-Gill cost NAS Succeeds in Offloading The one place where data management offloading was successful was Network Att Storage (NAS), NAS environments offload all the data management to centralized servers 16 (Josin: SUMMER 2018 VOL. 43, NO. 2 STORAG ocial Storage Devices: Offloading Data Management to Storage Devices that Can Act Collectively TOPAR 5876/105 Media

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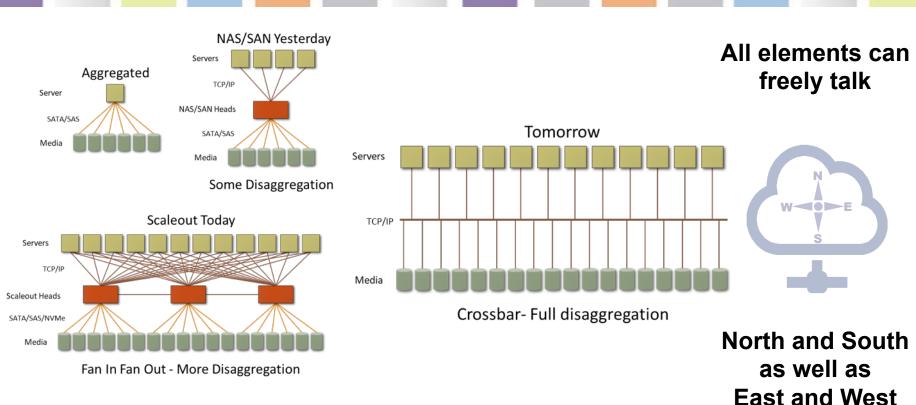
Eusocial Storage Devices

that Can Act Collectively

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Where this might go



## What's New in Container Storage? February 26, 2019 10:00 am PT Register at: https://www.brighttalk.com/webcast/663/345389



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