



SFF TWG

2025 Review and 2026 Plans

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SFF TWG Charter

- ❖ The SFF TWG develops technical specifications for:
 - ❖ Storage media
 - ❖ Networking
 - ❖ Pluggable solutions
- ❖ These specifications complement existing industry standards work and encompass:
 - ❖ Cables and connectors
 - ❖ Form factor sizes and housing dimensions
 - ❖ Management interfaces
 - ❖ Transceiver interfaces
 - ❖ Electrical interfaces
 - ❖ Related technologies
- ❖ These specifications enable technology vendors to produce compatible, multi-sourced products and solutions.
- ❖ For more info: <https://www.snia.org/sff>

SFF TWG 2025 Accomplishments

▸ We published or revised **20** specifications in 2025

Doc number	Title	Publication Revision	Publication Date
SFF-8665	QSFP+ 28 Gb/s 4X Pluggable Transceiver Solution (QSFP28)	2.0	November 24, 2025
REF-TA-1011	Cross Reference to Select SFF Connectors and Modules	1.3	November 17, 2025
SFF-TA-1005	Universal Backplane Management (UBM)	1.5	November 10, 2025
SFF-TA-1040	Internal Low Profile High Speed Cable Interconnect	1.0	November 4, 2025
SFF-TA-1036	Cable Optimized Boot Peripheral Connector for OCP M-PIC	1.0	November 4, 2025
SFF-TA-1027	QSFP2 Connector, Cage, & Module Specification	2.0	September 9, 2025
REF-TA-1011	Cross Reference to Select SFF Connectors and Modules	1.2	September 5, 2025
SFF-TA-1007	Enterprise and Datacenter 1U Long Device Form Factor (E1.L)	1.3	September 2, 2025
SFF-TA-1006	Enterprise and Datacenter 1U Short Device Form Factor (E1.S)	1.6	September 2, 2025
SFF-8472	Management Interface for SFP+	12.5	August 1, 2025
SFF-TA-1026	Storage System High Speed Cable Interconnect	2.0a	July 23, 2025
SFF-8024	SFF Module Management Reference Code Tables	4.13	July 11, 2025
SFF-TA-1042	Enterprise and Datacenter Device Form Factor (E2)	1.0	June 16, 2025
SFF-TA-1037	Connectors For Pluggable Multi-Purpose Module (PMM)	1.1	May 27, 2025
SFF-TA-1002	Protocol Agnostic Multi-Lane High Speed Connector	1.6a	May 16, 2025
SFF-TA-1009	Enterprise and Datacenter Standard Form Factor Pin and Signal Specification (EDSFF)	4.1a	May 4, 2025
SFF-9639	Multifunction 6X Unshielded Connector Pinouts	2.2	May 2, 2025
SFF-8679	QSFP+ 4X Hardware and Electrical Specification	1.9	April 21, 2025
SFF-TA-1033	Internal High-Speed Cable / Modular Connector System	2.0	March 28, 2025
SFF-TA-1032	Multi-lane External High Speed Cable System	1.0	March 3, 2025

SFF TWG Work Items -1

▸ We have **17** projects currently under development

Doc number	Title
SFF-TA-1045	Pin and Signal Definition for the SFF-TA-1044 Hybrid Orthogonal EDSFF Connector
SFF-TA-1044	Hybrid Orthogonal EDSFF Connector System
SFF-TA-1043	Copper Channel Modeling for AI
SFF-TA-1041	ULP Gen 7 PCIe Connector
SFF-TA-1039	PCIe FPP Hardware and Electrical Specification
SFF-TA-1038	Low Profile High Density Flexible Cable Connector
SFF-TA-1032	Multi-Lane External High Speed Cable Connector System
SFF-TA-1031	SFP2 Cage, Connector, & Module Specification
SFF-TA-1016	Internal Unshielded High Speed Connector System
SFF-TA-1008	Enterprise and Datacenter Standard Form Factor (E3)
SFF-TA-1006	Enterprise and Datacenter 1U Short Device Form Factor (E1.S)
SFF-9402	Multi-Protocol Internal Cables for SAS and/or PCIe
SFF-9400	Universal 4/8X Pinouts
SFF-8636	Management Interface for 4-lane Modules and Cables
SFF-8419	SFP+ Power and Low Speed Interface
SFF-8614	Mini Multilane 4/8X Shielded Cage/Connector (HDsh)
SFF-8613	Mini Multilane 4/8X Unshielded Connector (HDun)

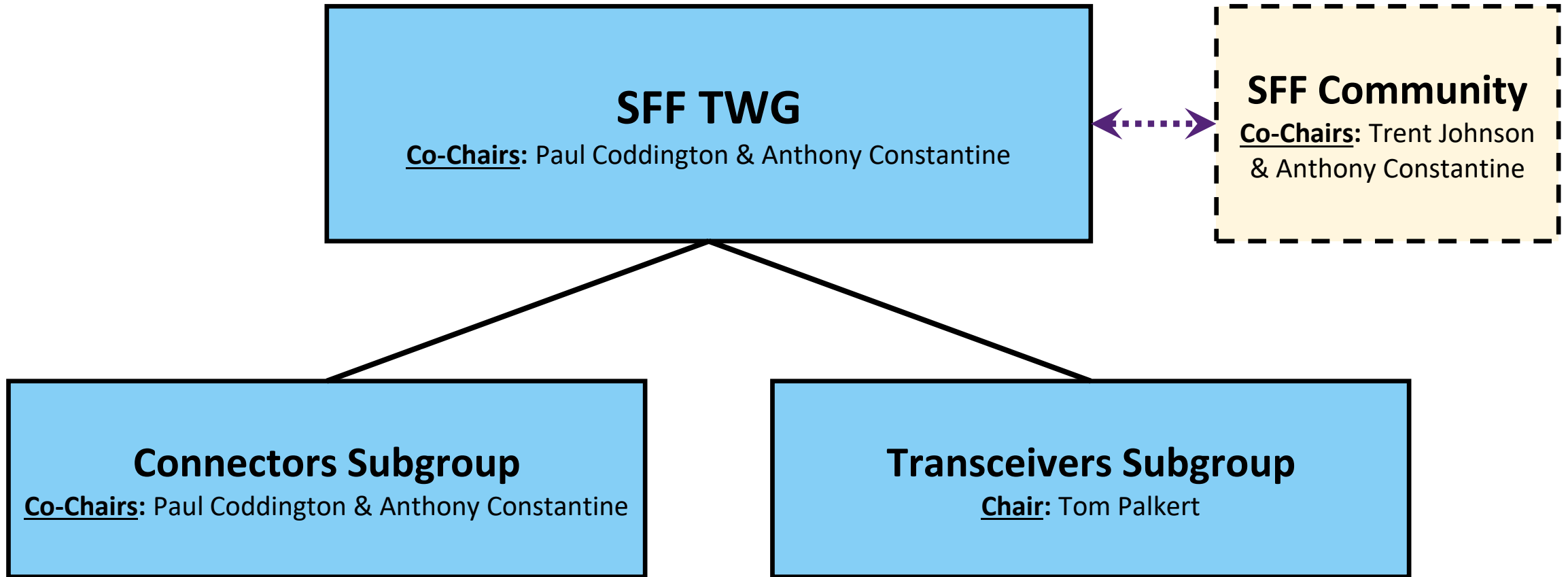
SFF TWG Work Items -2

- ▣ Known additional work items not started
 - ▣ EDSFF for PCIe 7.0
 - ▣ Characterization for liquid cooled SSDs
 - ▣ Continued partnership on 400G per lane development work for AI applications
- ▣ Collaborations With External Organizations
 - ▣ PCI-SIG
 - ▣ OIF
 - ▣ IEEE
 - ▣ Ethernet Alliance
 - ▣ JEDEC
 - ▣ OCP
 - ▣ INCITS T11 (Fibre Channel)

SFF TWG Membership: 73 Companies

SFF TWG Structure and Chairs



SFF TWG Participation

- We are solving problems around higher-speed Ethernet and PCIe interconnects to solve AI bottleneck problems while improving existing interconnects and form factors.
- Our members include participants involved in ASICs/CPU, Data centers, interconnects, networking, research, server systems, storage devices, test equipment, and transceivers.
 - We develop specifications to support a broad range of usages.
- Benefits:
 - Participation in development of SFF specifications, information documents, and reference guides
 - Ability to open new projects
 - Access to all presentations, all drafts, prior publications, and supplemental material relevant to all SFF projects
- Resources:
 - Public Site: <https://www.snia.org/sff>
 - Specifications: <https://www.snia.org/sff/specifications>
 - Additional questions? Please send mail to sff-chair@snia.org