



SDC 

STORAGE DEVELOPER CONFERENCE

SNIA  SANTA CLARA, 2017

Panel: Impact of Persistent Memory on SSDs and Fabrics

Abstract

- ❑ Persistent Memory is disrupting storage architectures. The low latency persistent impacts software, devices, and networking. This panel will focus on the impact to Solid State Devices and Fabrics. NVMe-oF is just now emerging on the scene, will it be capable of Persistent Memory over Fabrics? What do SSD Controllers require for the lower latencies and new channel speeds? How will the management of security be handled? A brief presentation from each of the panelists will be followed by Q&A with the audience.



Our Presenters



- Moderator:
 - Mark Carlson, SNIA Technical Council Co-Chair, Moderator



- Panelists:
 - Terry Hulett, VP Engineering & Systems Solutions Group, Everspin



- Rick Kumar, VP, Newisys



- Andrei Vityaev, Chief Strategy Officer, Mobiveil



Visit Our Panel's Demonstrations



- MRAM-Enabling Low Latency NVMe over Fabric Applications. NVMeoF with all standard drivers achieving surprising latency.



- FPGA based SSD Platform that incorporates PCI Express, NVM Express, LDPC, ONFI and DDR4 Controllers. The platform includes FPGA board, FMC Card, flash SO-DIMM and Hardware based FTL.



- An appliance that can be deployed today in support of disaggregate storage with best-in-class latency. Management software supports RESTful API that can be used for storage administration, in a very similar fashion to today's storage

