

September 23-26, 2019 Santa Clara, CA

Using SmartNICs as New Platform for Storage Services

Fazil Osman Broadcom Corporation

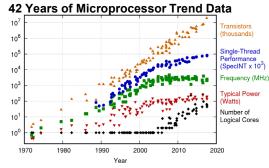


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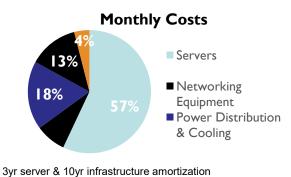
Why a SmartNIC

Moore's Law diminishing returns

- Vertical scaling power & cost model no longer viable
- CPU costs increasing
 - Economic benefits to limiting core count
- Multi-socket interconnect bottleneck
 - I/O, memory transactions across interfaces add latency
 - 2nd socket often used to get more memory and I/O
 - TCO penalty for 2nd socket
- Distributed cloud architecture
 - Smaller fault domains



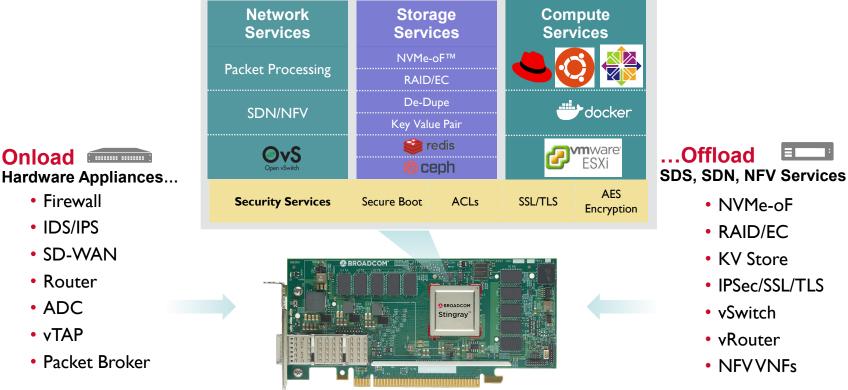
Original data up tot the year 2010 collected and plotted by M. Horowitz, F. Labonte, O. Shacham, K. Olukotun, L. Hammond, and C. Batten New plot and data collected for 2010 – 2017 by K. Rupp



Source: James Hamilton, AWS

What is SmartNIC

Architectural flexibility to quickly offload multiple overhead laaS services



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Onload

Firewall

IDS/IPS

Router

ADC

• vTAP

SD-WAN

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Evolution of SmartNIC...

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Pros

- Typical single-function offload
- Good performance

Cons

- Hard to design for performance
- Slow feature velocity (RTL)
- High power
- Large devices are expensive

HFT, HPC, Telco I/O

Network Function Processor

Pros

 More than single function

Cons

- Non-standard
 programming
- · Can be expensive

Telco I/O

• High power

Pros

- Performance/Watt
- General-purpose with standard programming

SmartNIC

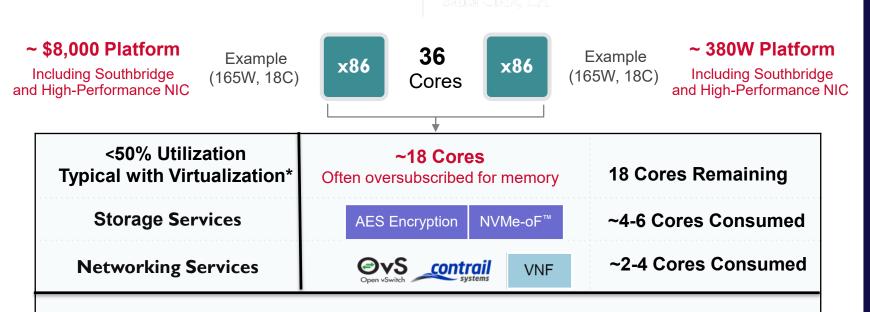
Great feature velocity

Cons

 Performance varies based on CPUs, DDR, and availability of integrated accelerators

Cloud DC & Telco

Platform Economics: CPU Workload Partitioning



Only 8-12 Cores Available for Applications

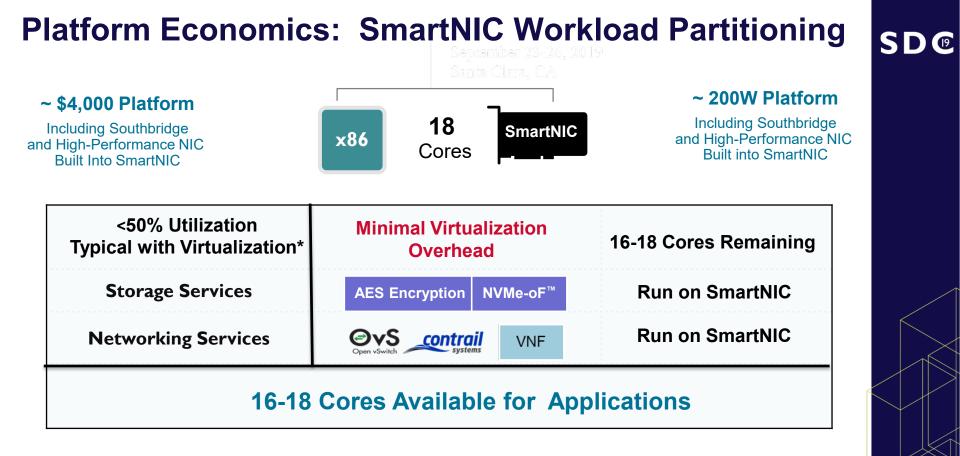
Services can consume most of the remaining cores

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* Gartner and Moor Insights &

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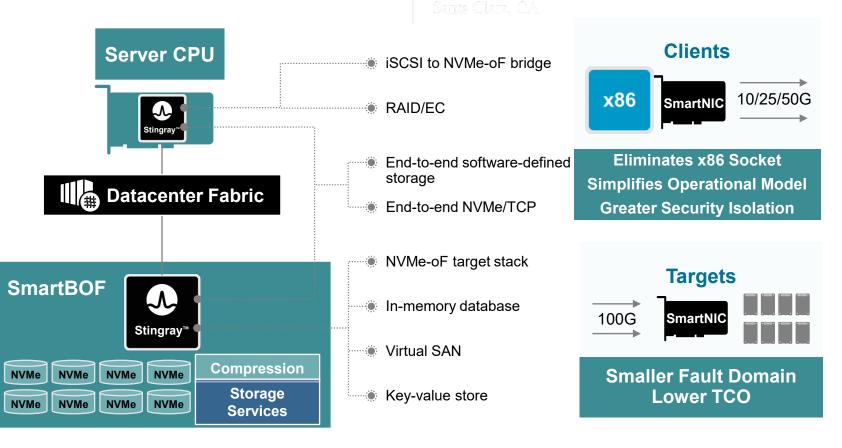
Offloading services to SmartNICs frees up cores for applications

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* Gartner and Moor Insights

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SmartNIC Storage Use Cases



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Example: Small vs. Large Fault Domains

Test Summary

Parameter	4x Stingray Targets	2 Socket-x86 Target
Network Link	4x 25G	1x 100G
NVMe SSDs (x2 Gen3)	32	30
4K Random Read	2.0M IOPS 🍟	1.8M IOPS
512K Sequential Write	37K IOPS 🛛 🍟	18K IOPS
Tail Latency (mean – P90% – P99.9%)	2 ms – 6.2 ms – 11 ms	2.3 ms – 12.9 ms – 23.5 ms
CPU+DRAM Power (estimated)	160W	300W

SmartNIC Disaggregated Storage Advantages

- Better performance
- Lower power
- Smaller fault domain reduces blast radius exposure (16TB vs 60TB)

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∘ NVMe-oF[™]

Stingray-Based Storage Platforms



Celestica Euclid AIC Manta Lymma

WDC F3100 and Stingray		
Low power		
NVMe-oF Enabled		
Software-programmable		
Low latency		

Up to 10 Modules and >20M IOPS

Scale Out

2U-24 Drive Systems Full HA Support

NVMe-oF Enabled

>10M IOPS

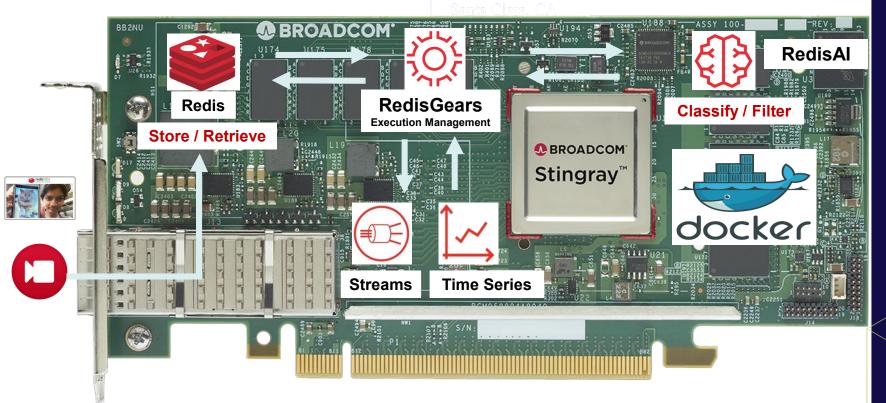
Enterprise Class

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RedisEdge on SmartNIC Use Case





Deployed within hours using standard container

SmartNIC in NVMe-oF[™] – We Have Come A Long Way

but...

SO

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OS Support for NVMe-oF

- Limited to recent versions of Linux
- No announced support for other operating systems





University of New Hampshire InterOperability Laboratory



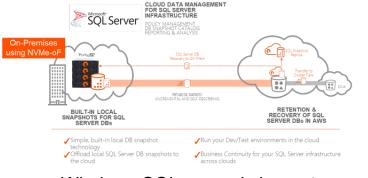
NVMe/TCP™ Ratified Nov 2018

Ecosystem is maturing but broad adoption requires solution to OS support problem

Pure Storage Use Case

Bringing NVMe over Fabrics to Windows client solutions

SQL SERVER DB SNAPSHOT PORTABILITY WITH CLOUDSNAP



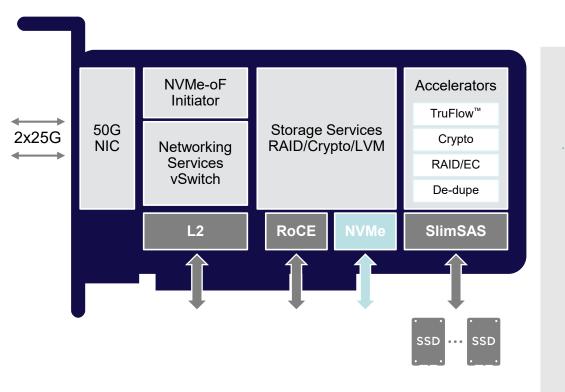
STINGRAY ENABLING NVME-OF[™] FOR PURE FLASHARRAY//X



Windows SQL server is largest application with Pure customers

Problem: Windows does not support NVMe-oF natively limiting FlashArray scalability Solution: Co-developed Windows iSCSI to NVMe-oF on Stingray SmartNIC

Broadcom Glass Creek Adapter



Applications

Introducing

Glass Creek

- Storage disaggregation for any OS
- Works with standard NMVe drivers
- Storage virtualization
 - Bare metal and virtualized servers
- Storage services offload
 - Logical Volume Management
 - RAID/EC, De-dupe, Crypto

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Stingray Solutions SDC

NVMe-oF SmartBOFs

- High performance fabric storage appliances
- Enterprise-capable high-availability solutions
- Cloud scale with low blast radius

Expanding NVMe-oF ™ ecosystem

- Multiple vendors
- NVMe-oF and now NVMe virtualization

Highest performance SmartNIC

- System architecture, cost and performance
- End of Moore's Law
- Dual socket architectures are inefficient

SmartNIC Use cases

- General-purpose programmability
- Offloading storage and networking services
- Bare metal and virtualization servers
- Security