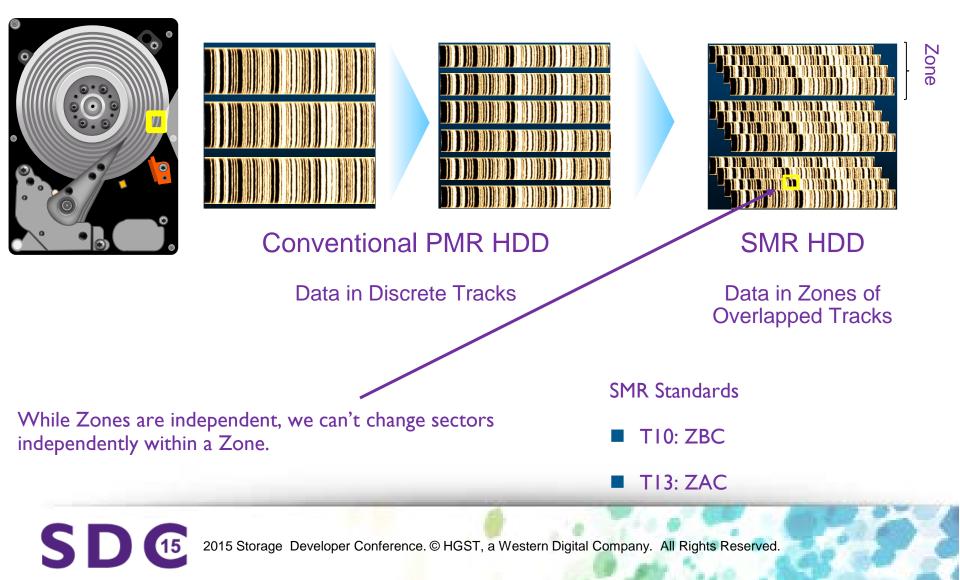


SMR: The Next Generation of Storage Technology

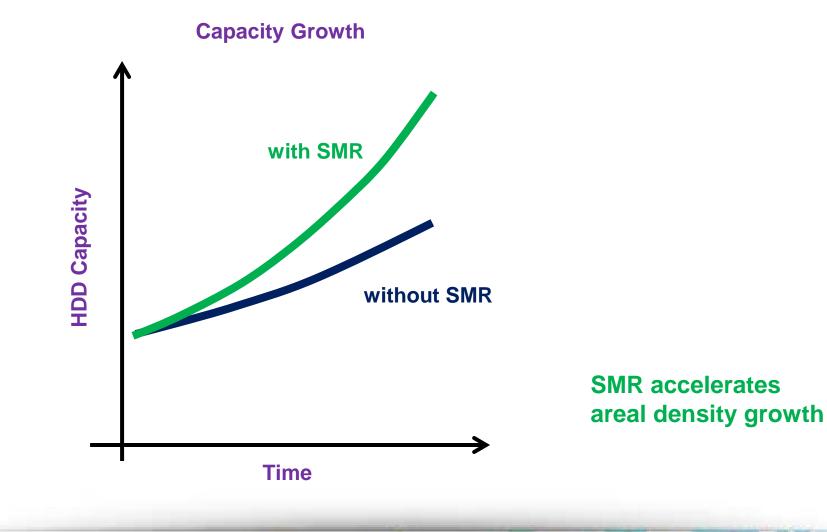
Jorge Campello HGST, a Western Digital Company

What is Shingled Magnetic Recording?



Why SMR?

SD (6



So How do We Deal with the Writes?

Can't the drive manage it internally?
 Drive Managed Model

Can't we just present the host system with the constraint and let the host manage it?

Host Managed Model

Isn't there something "in between"?
Host Aware Model

4

Disk Models

SD (E

Three models are defined

Model	Description	Impact on Host Software		
Drive Managed	 Disk firmware handles random writes processing Backward compatible (standard Device Type 0H) No host changes necessary <u>Performance can be unpredictable</u> in some workloads 	NONE		
Host Managed	 Host uses new commands and information to handle write operations Not backward compatible (Device type 14h) Predictable Performance 	Host SW must write sequentially to the disk		
Host Aware	 Disk firmware handles random writes processing Backward compatible (standard Device Type 0H) Host uses new commands and information to optimize write behavior "or" host can treat device as an autonomous device Performance can be unpredictable if the host sends a "suboptimal" request 	NONE ~ HIGH Depends on the amount of optimization		

SMR Drive Model

The drive is divided into zones

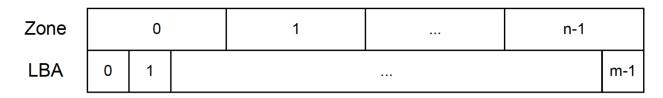


Figure 2 — Zones in a zoned block device

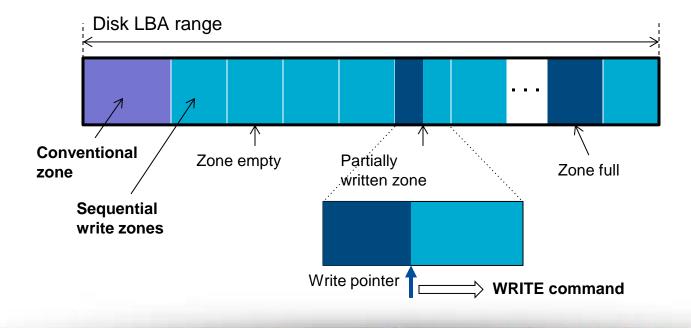
- Three types of zones
 - Conventional: Works like traditional PMR
 - Write Pointer Zones: They have a write pointer indicating the position for the next write. Two types
 - □ Sequential Write Required: Writes must be sequential in zone
 - □ Sequential Write Preferred: Writing at write pointer not required.

		lowest LBA				highest LBA		
				write pointer	1			6
SDO	15	2015 Storage	e Developer Conferer	nce. © HGST,	a Western Digital Com	npany. All Rig	hts Reserved.	1

SMR Drive Model

SD (E

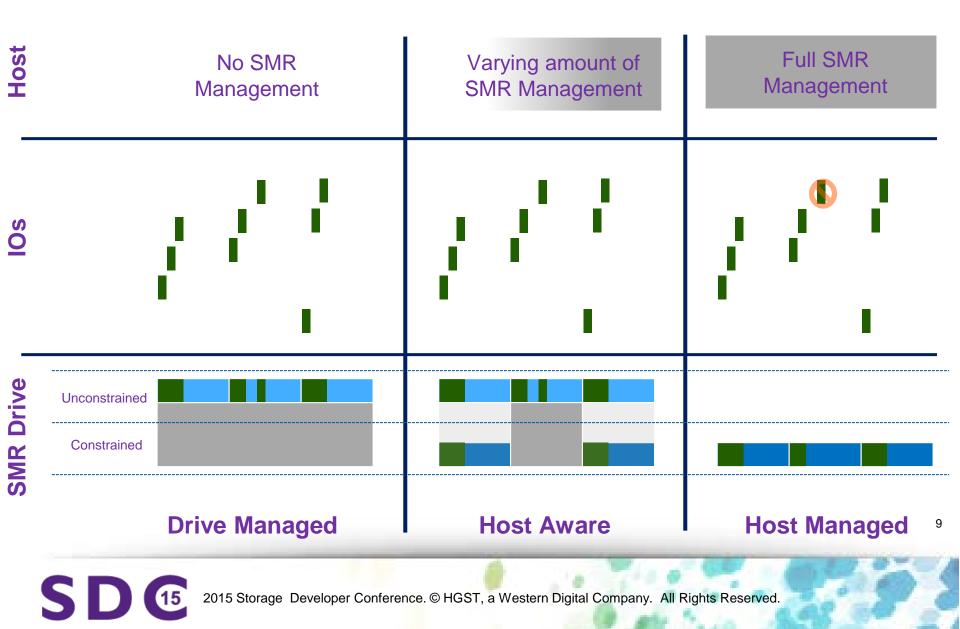
Zone Type	Disk Model	Characteristics		
Conventional zone	Host-aware and Host-managed	Unconstrained writes		
Sequential write preferred	Host-aware	Unconstrained writes possible		
Sequential write required	Host-managed	Write operations MUST BE sequential		



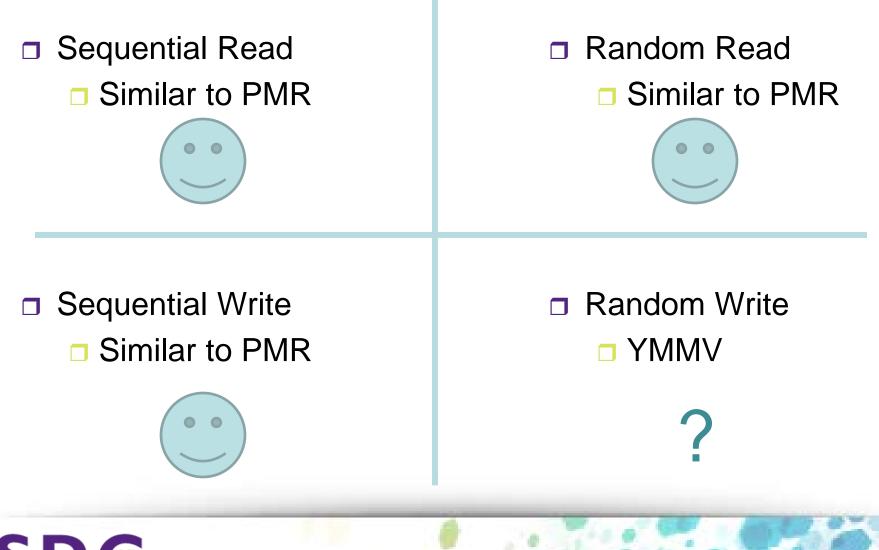
ZBC/ZAC Command Set

- Two main commands
 - **REPORT ZONES**: discover disk zone configuration and zone write pointer position
 - RESET WRITE POINTER: reset a zone write pointer position to the beginning of a zone
 - Destructive operation: data previously written in the zone becomes inaccessible
- Disk write performance in sequential write zones can be optimized using additional commands
 - OPEN ZONE: keep a zone FW resources locked until the zone is closed
 - **CLOSE ZONE**: release a zone FW resources
 - FINISH ZONE: fill a zone

SMR Implementation Models



Drive Managed Model



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10

Drive Managed Model: Random Write

Small Block

Performance dominated by seek time. Caching writes on media and moving later has good performance.

Large Block

Seek time no longer dominates. Writing twice has overhead. High duty cycle fills up cache quickly and doesn't allow time for recovery.



Huge Block

Behaves close to sequential writes.



Performance dominated by seek time. Caching writes on media and moving later has good performance.

Seek time no longer dominates. Writing twice has overhead. Low duty cycle allows drive to hide overhead.

Behaves close to sequential writes.



Low Duty Cycle

Host Managed Model

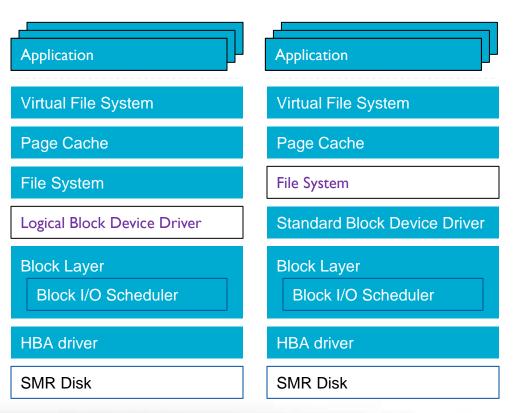
- New device type.
 - Not backwards compatible
 - Doesn't work with existing software
- How do we make use of Host Managed SMR?
 - HW Solutions: HBA, appliance, etc, virtualizes
 - SW Solutions:
 - □Kernel level support
 - □ Application level support

Kernel Level Support

- Basically, two approaches here
 - Device mapper logically standard storage device
 - » STL: Similar to flash FTL
 - » Allows reusing existing software components (e.g. file systems)
 - SMR compliant file system
 - Combination of both

Currently

- Support for XFS announced
- Other FS branches and news FS projects started.
- Device mapper implementations exist.
- Zone caching schemes being investigated.



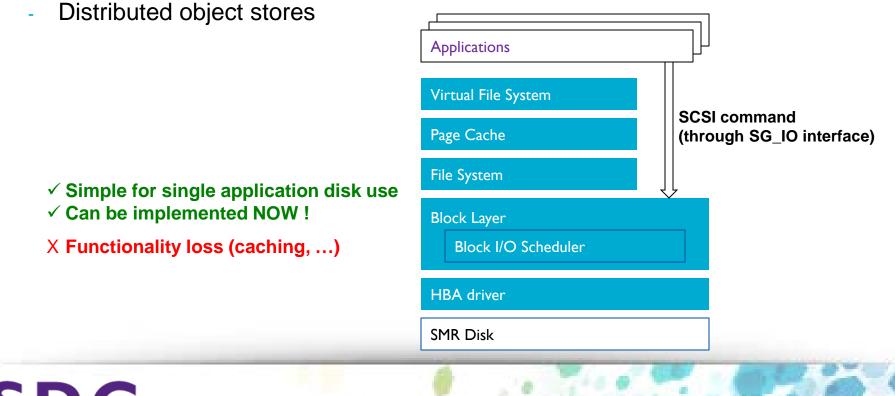
http://xfs.org/index.php/File:Xfs-smr-structure-0.2.pdf



- Transparent to applications: no modifications necessary
- X Implementation will take time

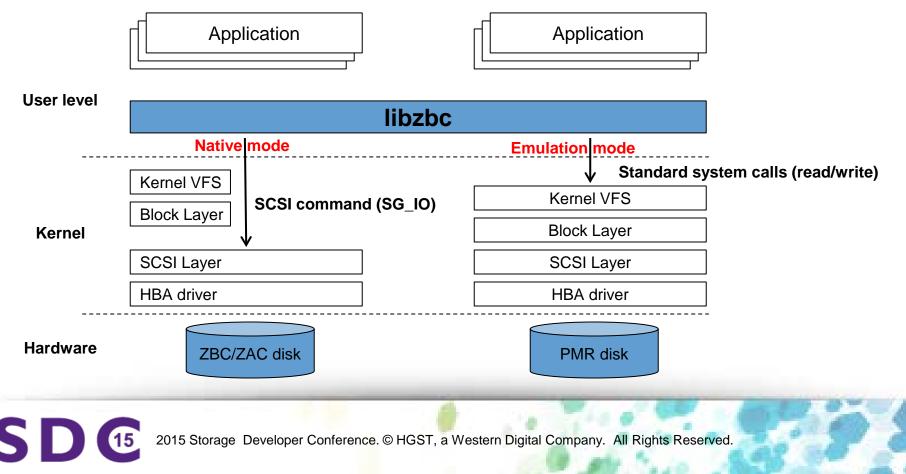
Direct Application Management

- Direct disk accesses from applications through SG_IO
 - Application level code issues ZBC commands and write-pointer aligned write operations through SG_IO interface
 - Kernel level support is minimized
 - » Device type recognition on HBA bus scan for device SG node creation
- Fits many use cases were kernel file systems are not strongly necessary



Libzbc Open Source Project

- User space library providing applications with direct access to ZBC/ZAC drives.
- Enabling application level innovations!



Libzbc Project

Download NOW: http://github.com/hgst/libzbc



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	🛄 hgst / libzb	c		★ Star 6 ¥ Fork 3		
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	ill include	Changed libzbc license to BSD 2-clause.	27 days ago	III Graphs		
	iin ib	zbc_ata_classify was returning the number of report zones log pages	25 days ago	000		
	iin m4	Forgot adding these files	27 days ago	https://github.cc 000		
	in tools	Enabled compilation warnings (-Wall -Wextra -Wno-unused-parameter)	a month ago	You can done with HT 000		
	🖹 .gitignore	Merged zbz_need_reset and zbz_non_seq fields in zone information	3 months ago	Subversion. (0) 000		
		Changed libzbc license to BSD 2-clause.	27 days ago	Clone in De 000		
Command Description						
			4 months ago	000		
			a month ago	000		
			18 days ago	000		
eport zo	noc	Display a disk zone	4 months ago	000		
:pon_20	nes	information	27 days ago	000		
			4 months ago 4 months ago	000		
		Reset a zone or all zones wr		000		
eset_writ	te_pointer			000		
	-	pointers		000		
		B				
ead_zone	9	Read data from a zone		Zone St		
rite_zon	е	Write data or a file to a zone		Com		
t_zones		For emulation mode: configure the disk zones		256 R		
t_write_p	oointer	For emulation mode: manually change the value of a disk write pointer	2	Zone i		

zbc_re

zbc rea

zbc re zbc wr

zbc set

zbc_set

SD (15

0	00				X ZBC Device Zone S	tate		
-/d	lev/sdc: 8	192 zones						
		Туре	Condition	Need Reset	Non Seq	Start LBA	Length	Write pointer LBA
		Conventional	Not WP	No	No	37224448	524288	N/A
uests	00072	Conventional	Not WP	No	No	37748736	524288	N/A
	00073	Conventional	Not WP	No	No	38273024	524288	N/A
	00074	Conventional	Not WP	No	No	38797312	524288	N/A
	00075	Conventional	Not WP	No	No	39321600	524288	N/A
	00076	Conventional	Not WP	No	No	39845888	524288	N/A
a.	00077	Conventional	Not WP	No	No	40370176	524288	N/A
tub.cc	00078	Conventional	Not WP	No	No	40894464	524288	N/A
th HT	00079	Conventional	Not WP	No	No	41418752	524288	N/A
	00080	Seq write req.	Open	No	No	41943040	524288	41943048
in De	00081	Seq write req.	Full	No	No	42467328	524288	18446744073709551615
nloac	00082	Seq write req.	Empty	No	No	42991616	524288	42991616
	00083	Seq write req.	Open	No	No	43515904	524288	43778048
	00084	Seq write req.	Empty	No	No	44040192	524288	44040192
	00085	Seq write req.	Empty	No	No	44564480	524288	44564480
	00086	Seq write req.	Empty	No	No	45088768	524288	45088768
	00087	Seq write req.	Empty	No	No	45613056	524288	45613056
	00088	Seq write req.	Empty	No	No	46137344	524288	46137344
	00089	Seq write req.	Empty	No	No	46661632	524288	46661632
	00090	Seq write req.	Empty	No	No	47185920	524288	47185920
	00091	Seq write req.	Empty	No	No	47710208	524288	47710208
-	00092	Seq write req.	Empty	No	No	48234496	524288	48234496
	00093	Seq write req.	Empty	No	No	48758784	524288	48758784



Zone index 0 - + Beset Write Pointer

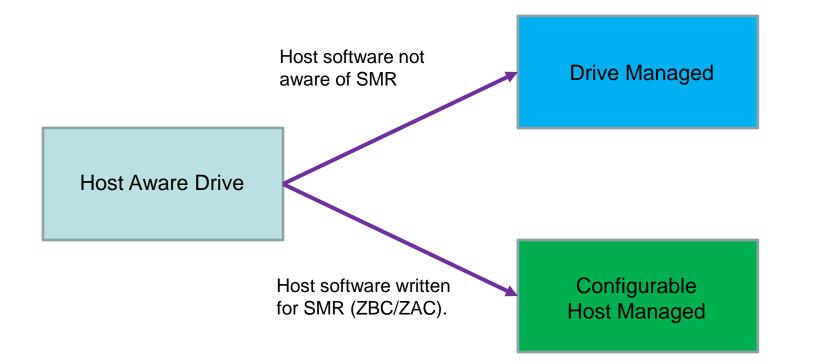
+ 0 =

📖 Refresh 📃 Exit

Host Aware Model

- Backwards Compatible
- Implements ZBC/ZAC
- □ In essence, a hybrid Model.

Host Aware Model





The SMR Opportunity

Those who learn how to manage the SMR constraint first will be the new leaders in mass storage solutions.

New storage solutions designed with SMR in mind will have a technological advantage.



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