

Consideration for adopting NVMeF for Enterprise Storage

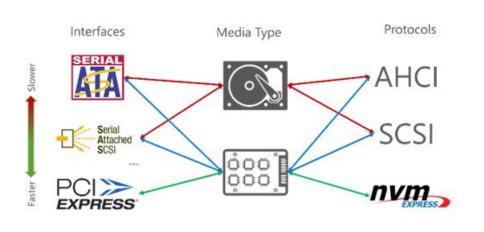
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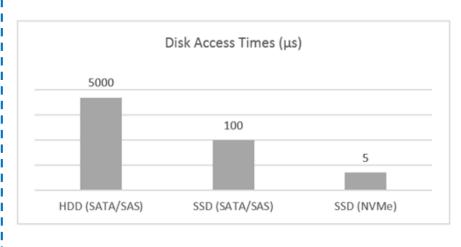
Software Product Engineering, HiTech, Tata Consultancy Services

Agenda

Current Storage Architecture & Network limitation NVMe Over Fabric Solution Comparison for different NVMe fabric Requirements for running End to End NVMeF solutions

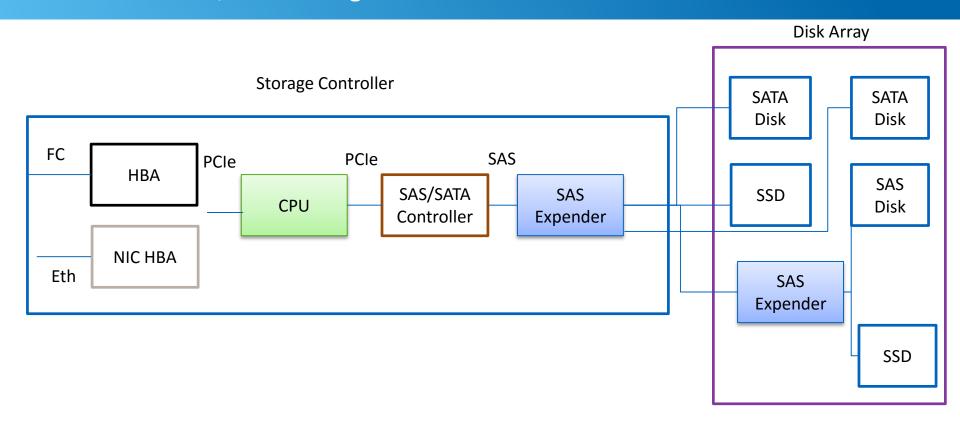
Recap of Communication Protocols



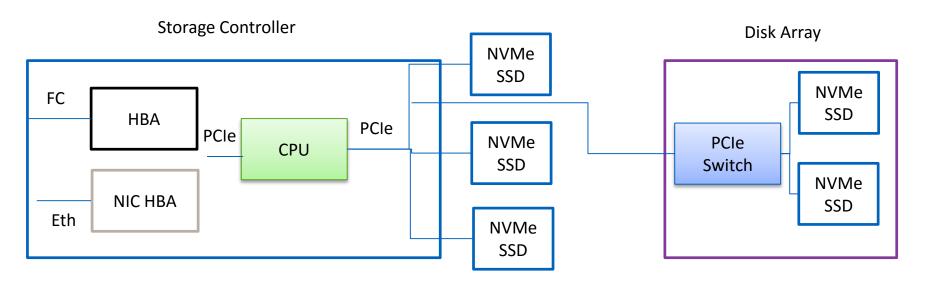


- SAS and SATA performance increased over time but protocols have not changed much
- NVMe was created to allow direct access to the logical block based architecture of SSDs and to do highly-parallel IO enabling SSD to execute more IO threads than any other protocol before.

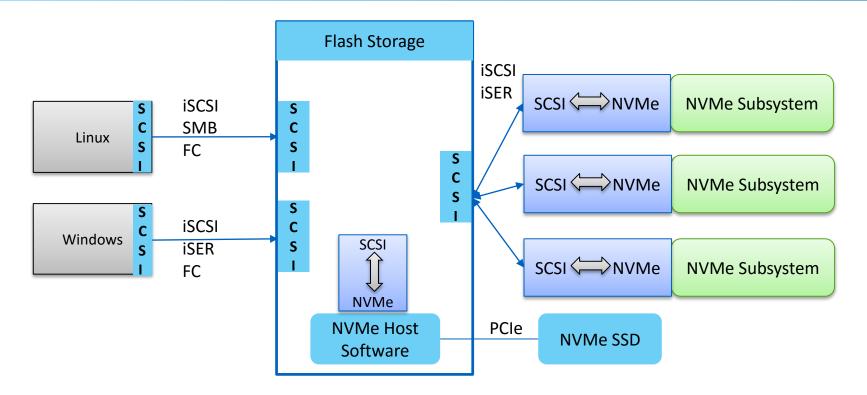
Traditional SAS/SATA Storage Architecture



Next Gen NVMe Storage Architecture



Data Flow with Enterprise Storage Over Network : Limitations



Protocol conversion bridge is required to access the data over network which increases the NVMe latency

Why NVMe Over Fabric Solution?

Defines a common architecture that supports a range of storage networking fabrics for NVMe block storage protocol over a storage networking fabric
No translation to or from another protocol like SCSI
Inherent parallelism of NVMe multiple I/O Queues is exposed to the host
NVMe commands and structures are transferred end-to-end
Maintains the NVMe architecture across a range of fabric types
Maintains architecture and software consistency between fabric types by standardizing a common abstraction and encapsulation definition

Design goal of NVMe over Fabrics:

Provide distance connectivity to NVMe devices with no more than 10 microseconds (µs) of additional latency

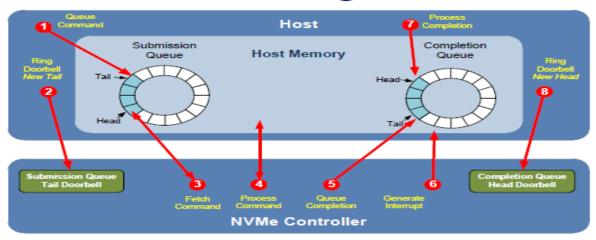
NVMe protocol – An Overview

- Standardization began in 2009
- ☐ Standardizes the interface between CPU and PCIe attached SSD
- □ Version 1.0 was released in 2011. Version 1.3 launched on 1st May 2017. Complete versions are publically available at http://www.nvmexpress.org/specifications/
- ☐ Tuned for performance and strong support for many CPU architecture and OS.

Working Principle of NVMe Protocol

- Host writes command to submission queue
- Host writes updated submission queue tail pointer to doorbell
- Controller fetches command
- 4. Controller processes command
- Controller writes completion to completion queue
- 6. Controller generates MSI-X interrupt
- Host processes completion
- 8. Host writes updated completion queue head pointer to doorbell

Efficient Queuing Interface



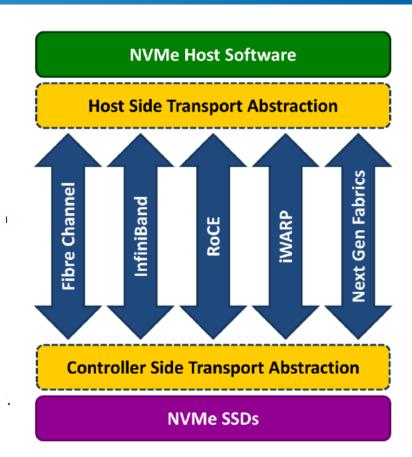
Source: nvmexpress.org

NVMe commands is just 64 bytes of data in your memory while response comes in 16 byte

Fabric Supported by NVMe

Two types of fabric transports for NVMe are currently under development:

- NVMe over Fabrics using RDMA(Infiniband, iWARP, RoCE)
- NVMe over Fabrics using Fibre Channel (FC-NVMe)



Source: nvmexpress.org

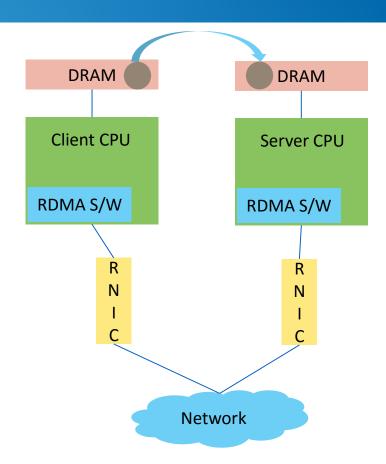
What is RDMA(Remote Direct Memory Access)?

Each QP has a Send Queue (SQ) and Receive Queue (RQ)

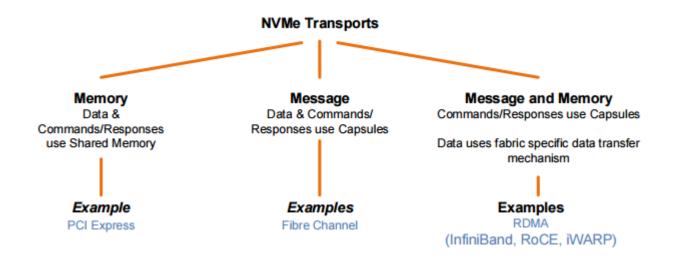
RDMA is a combination of hardware, OS code and user space software that enables efficient, high-performance access to remote memory RDMA presents a standard programming interface to user and then uses hardware to perform memory access at distance and scale. RDMA runs over a variety of different physical layers and can sit on top of TCP/IP(iWARP), Ethernet(RoCE) and Infiniband Initially developed for HPC, it is now becoming popular in data center environment. Applications communicate with the RDMA NIC using dedicated Queue Pairs (QPs) and Completion Queues (CQs) Each application can have many QPs and CQs

How does RDMA works?

- Client establish connection to server using rdma command
- Both client and server register memory region on their own DRAM
- Client and server exchange permission and security information on those memory regions
- Using client and server then ping-pong incrementing data back and forth between two memory regions



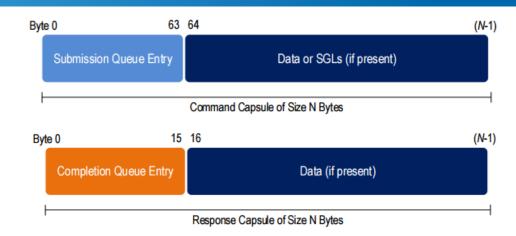
NVMe Over fabric Protocol – An Overview



Source: nvmexpress.org

NVMe Over fabric commands flow

- Host fetch the 64 Bytes of commands from Driver
- Host creates the capsules that contains the commands and optional data
- Capsules are transported over relevant fabric(FC or RDMA)
- Capsules supports admin commands, IO commands(NVMe commands) and fabric specific commands
- When data is not contained in the capsules, the command contains information like key/offset to locate that data on host

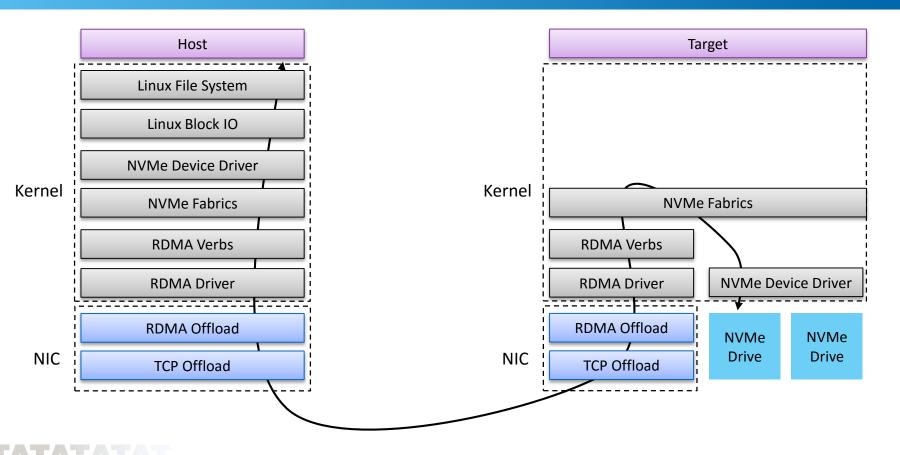




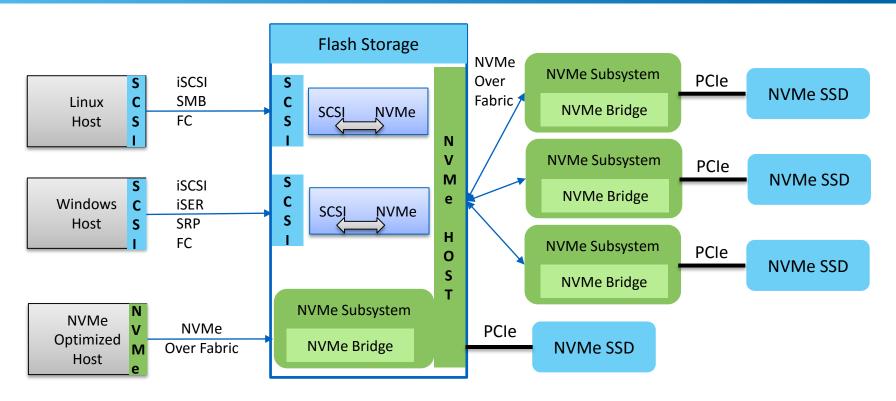
* 90% of NVMe Over Fabric commands are same as NVMe

Source: nvmexpress.org

How Data Flows from Host to Target in NVMeF?



End to End NVMe Over Fabric Solution for Enterprise Storage



Front End Fabric

Back End Fabric

Matrix for best suitable fabric

Fabric	Vendors	Transport	Pros	Cons
Infiniband	Mellanox	Infiniband	Lowest latency High security	Highest Cost Low Volume
iWARP	Chelsio	TCP/IP	Cheap	Highest Latency Not Scalable
RoCE/Routable RoCE(V2)	Mellanox Avago Cavium	Converged Ethernet	Datacenter - Preferred	Non-Legacy Equipment
FC	Cisco	Fiber Chanel	Full Compatible with SCSI and FC High security	Upgraded switch and HBA

NVMe Over Fabric supported Products – Sample List

Α	r	r	a	V	/S
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- Supermicro
- Mangstor
- E8 Storage
- Pavillion Data
- Excelero
- Aperion

Adapters

- Mellanox supports RoCE
- Chelsio supports iWARP
- Qlogic supports iWARP and RoCE

Reference Design

- Seagate
- WD
- Toshiba
- Micron
- Kingston
- Samsung

Conclusion

"Flash is technology of choice for Storage, NVMe is protocol for Flash, Storage network is the new bottleneck where NVMe Over Fabric is the solution "

Moving on NVMeF Solution require:

- NVMe Over FC Requirement
 - Fibre Channel Gen 5 and Gen 6 switches supported, full compatibility with SCSI & NVMe over FC
 - Generation 6 HBA's with new devices drivers required to support NVMe over fabrics, concurrently along with SCSI
- ☐ NVMe Over RDMA Requirement
 - iWARP requires iWARP specific RDMA NIC's and device drivers
 - InfiniBand requires both IB HBA and IB switches
 - RoCE requires DCB Ethernet switches, along with driver support in NICs

Plenty of demos for NVMe over Fabric supported products has been done in Flash Memory Summit and Intel Developers Forum in 2016 where preferred choice of fabric was RoCE solution.

Experience certainty



Q&A mail us @ sanjeev24.k@tcs.com

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Thank You