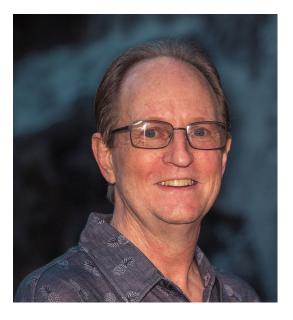
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Computational Storage – Driving Success, Driving Standards

A SNIA Webcast Discussion with Bill Martin, Samsung; Jason Molgaard, Arm; Oscar Pinto, Samsung; Scott Shadley, NGD Systems

Live October 26, 2021 at 10:00 am PDT

Today's Speakers



Bill Martin Samsung Semiconductor Inc. Editor, SNIA Computational Storage Architecture and Programming Model



Jason Molgaard Arm Co-Chair, CMSI Computational Storage Technical Work Group



Oscar Pinto Samsung Semiconductor Inc. Editor, SNIA Computational Storage API Scott Shadley NGD Systems Co-Chair, CMSI Computational Storage Technical Work Group



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SNIA-at-a-Glance









50,000 IT end users & storage pros worldwide



Computational Storage -A Quick History and Status



Common Language, Common Goals

- The challenge with driving new technology can be the convolution of data
 - The ability to say the same thing with different words
- Computational Storage had many names back as far as 2010
 - Scale-In
 - In-Situ Processing
 - Compute to Data
 - In-Data Processing
- A change to the taxonomy model was needed and a SNIA Technical Work Group (TWG) was formed







The Ongoing Work of SNIA to Define Standards

TWG Working group is continuing to see growth

- Member count is up, Users 'following' and 'participating'
- 51 companies, 261 individual members
- Work in the Special Interest Group
 - CS SIG Webinars, Blogs, Events

- Collaborating with other Groups
 - NVM Express Computational Programs
 - Be sure to check out the <u>Storage Developer Conference session presented by</u> <u>the Co-Chairs on that work</u>



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51 Participating Companies - 261 Member Representatives

The Efforts to Get Information Out is Continuing

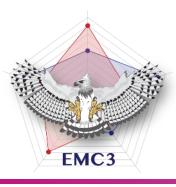
ComputerWeekly.com

13-part Series



- Gartner Analysts 2018 and 2021 'Cool Vendor'
 - Hype Cycle Entry

Sponsored Efforts



Key Solution Elements	
Computational Storage	Parallel Database with integrated Analytics
Embed compute with storage, offloading main server, improving performance on smaller systems by reducing data transfer to main system and enabling on-chip intelligence	Query across NVMe devices in parallel, making effective use of computational storage. Embedded analytics allowing analytics free of resources on the main system. Seamless replication of data to backup

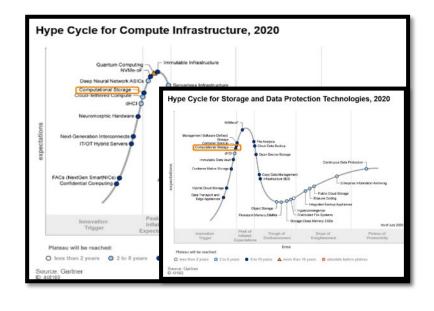


phere & Bitfusior Ability to offer Edge resilie

with vSAN, HA, FT. GPU acceleration for computationa orage w/ Bitfusion Effective use of limited hos

IJ **CW Developer Network**

Computational storage: A Computer Weekly analysis series





CW Developer Network

Computational storage series: Evaluator Group Speculations, expectations & extrapolations

CW Developer Network

Computational storage: NGD Systems / SNIA -Icebergs at the Edge

Cliff Saran's Enterprise blog

An opportunity to redesign computer architectures

How computational storage delivers datacentre benefits

Computational storage is an emerging field of IT that features compute processing power closely coupled with storage. We look at what it can be used for



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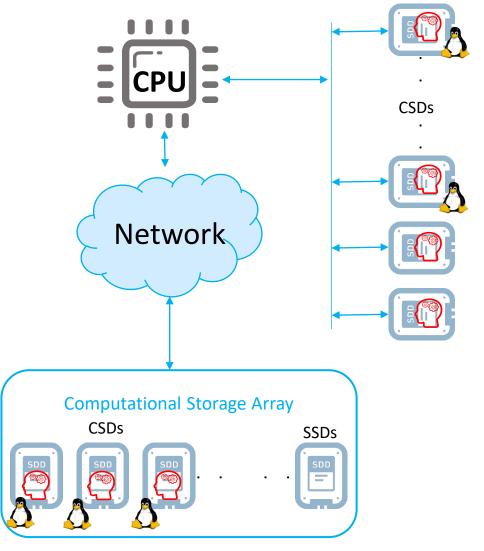
vmware

Computational Storage -The Work Being Done



A Potential Use Case for Computational Storage

- Generate Metadata database (e.g tags) over a large set of unstructured data locally stored on the drive, with an integrated AI inference engine
- Operation may be:
 - Triggered by a host processor
 - Done offline as a background task (batches)
- Metadata database may be then used by upper layer Big Data Analytics software for further processing
- Can work both on direct attached storage or on remote over the network storage
- Examples: Video search, Ad insertion, Voice call analysis, Images, Text scan, etc



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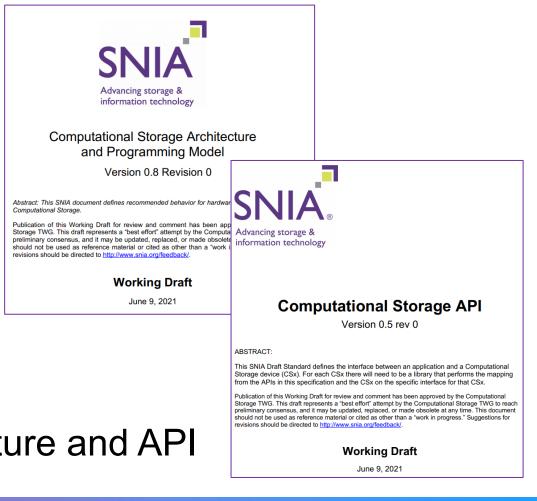
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Current Progress of TWG Output

Architectural Document has been released

- V0.8 is now in Public Review
- Many updates from 0.5
- API Document has been released
 - V0.5 is now in Public Review
- Security now being reviewed
 - In collaboration with SNIA Security TWG

Today we'll be speaking about Architecture and API



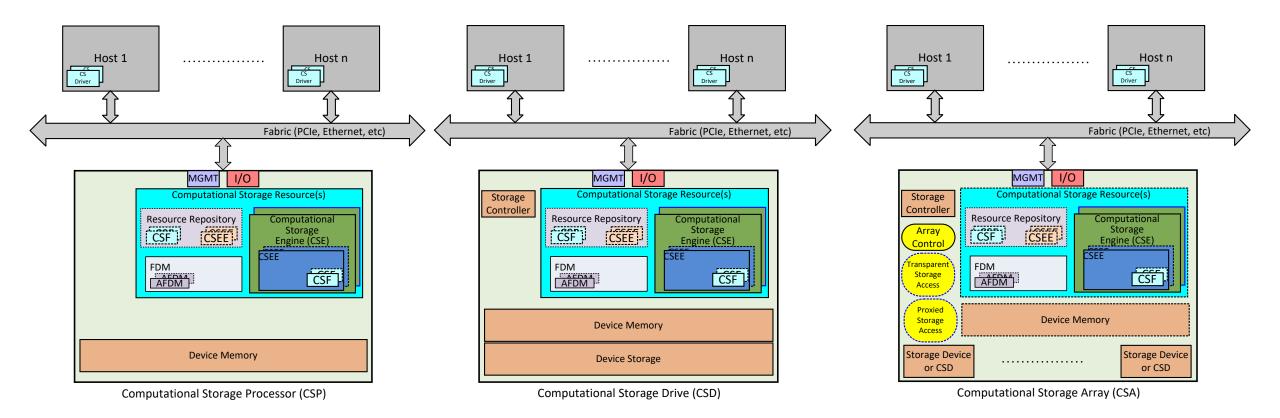
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The Taxonomy of Computational Storage

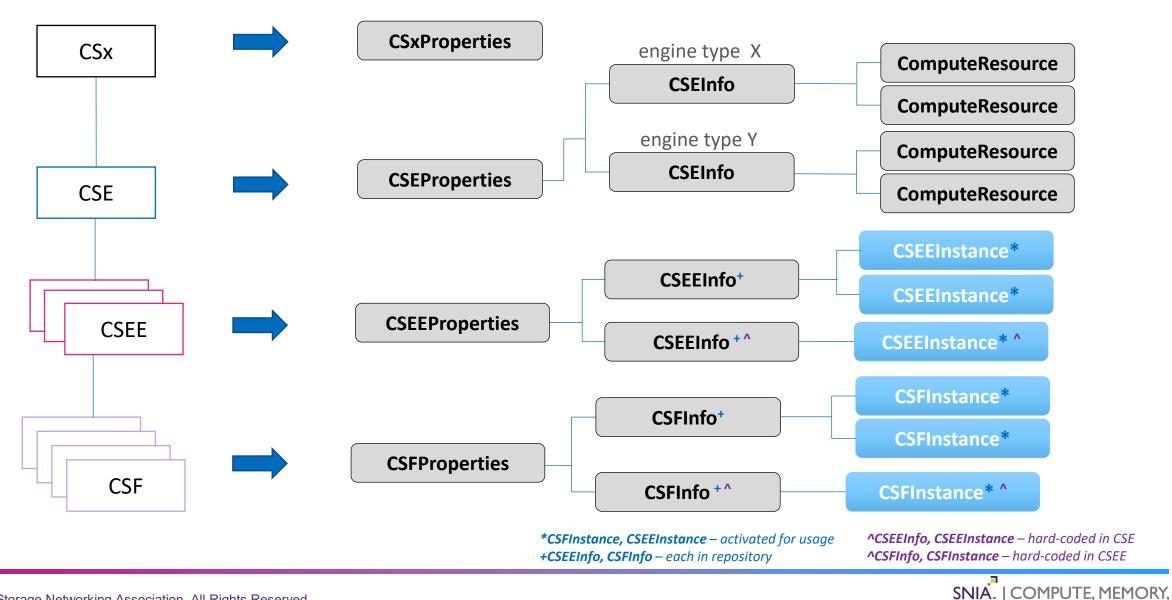




Computational Storage Architecture



Implementing The Taxonomy of Computational Storage



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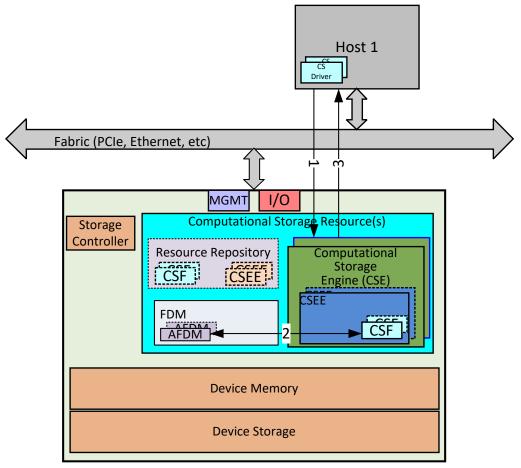
Direct Computational Storage Implementation

Assumption

- Data on which computation is to be performed is placed in the FDM, prior to the request to the CSE, through some process that is not shown in this figure
- Result data, if any, is returned to the host through some process that is not shown in this figure

Process

- 1. The host sends a command to the CS controller to invoke the CSF;
- 2. The CSE performs the requested computation on data that is in FDM and places the result, if any, into FDM; and
- 3. The CSE returns a response to the host.





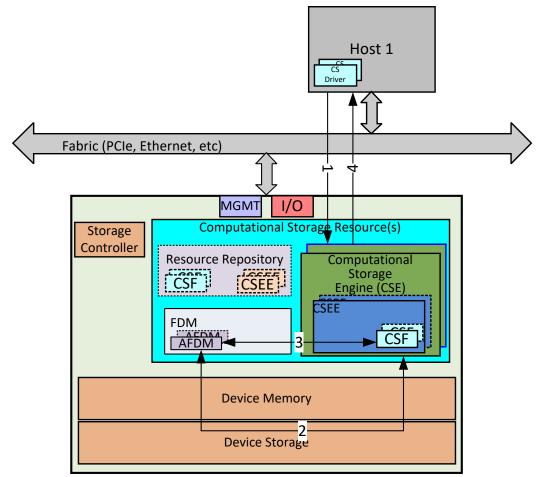
Computational Storage on Device Data

Assumption

 This example is for a computation on data that is in device storage

Process

- 1. The host sends a command to invoke the CSF;
 - a. The command specifies the Device Storage location of the data;
- 2. The CSE moves data from Device Storage to FDM;
- 3. The CSE performs the requested computation on data that is in FDM and places the result, if any, into FDM; and
- 4. The CSE returns a response to the host.

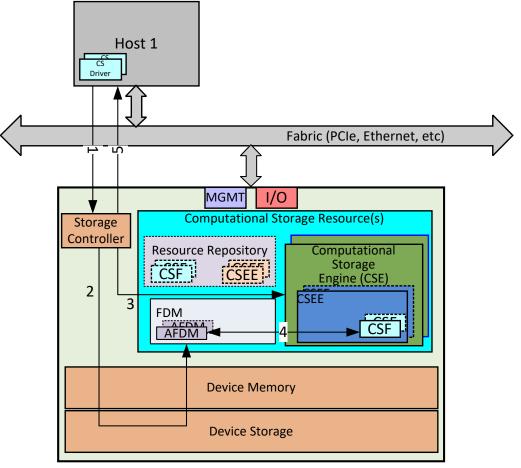




Indirectly Using Computational Storage on Device Data

Assumption:

- This example is for a device to host operation
- Process
 - 1. The host sends a storage request to a Storage Controller where:
 - a. that storage request is associated with a target CSF; and
 - b. the storage controller determines what CSF is associated with the storage request;
 - 2. The Storage Controller moves data from storage into the FDM;
 - 3. The Storage Controller instructs the CSE to perform the indicated computation on the data in the FDM;
 - 4. The CSE performs the computation on the data and places the result, if any, into the FDM; and
 - 5. The Storage Controller returns the computation results, if any, from the FDM to the host.





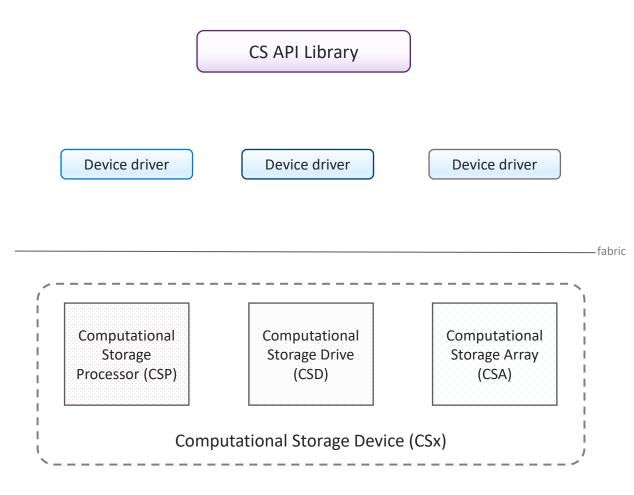
Computational Storage APIs



CS API Library Overview

One Set of APIs across all CSx types

- CSP, CSD, CSA
- Common set of APIs for different CS devices
- One interface to different device and connectivity choices
 - Hardware ASIC, CPU, FPGA, etc
 - NVMe/NVMe-oF, PCIe, custom, etc
- Configurations may be local/remote attached
- Hides vendor specific implementation details below library
- Abstracts device specific details
- APIs to be OS agnostic





About API Library

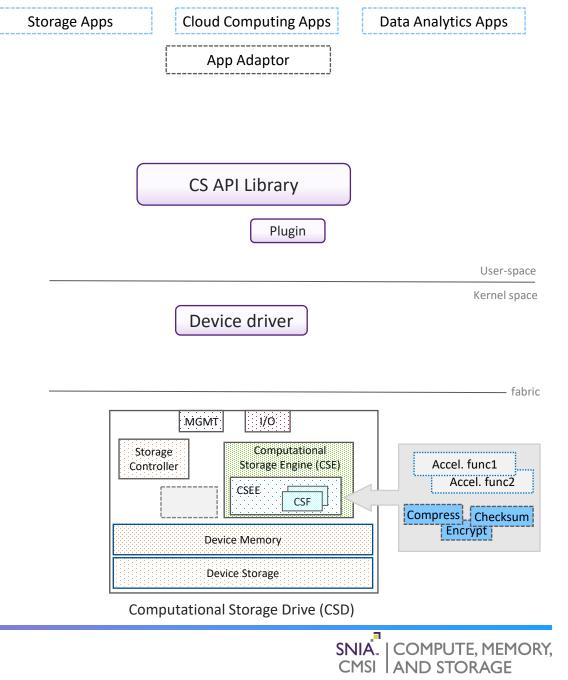
- Uniform interface for multiple configurations
 - APIs provided in common library

Each CSx managed through its own device stack

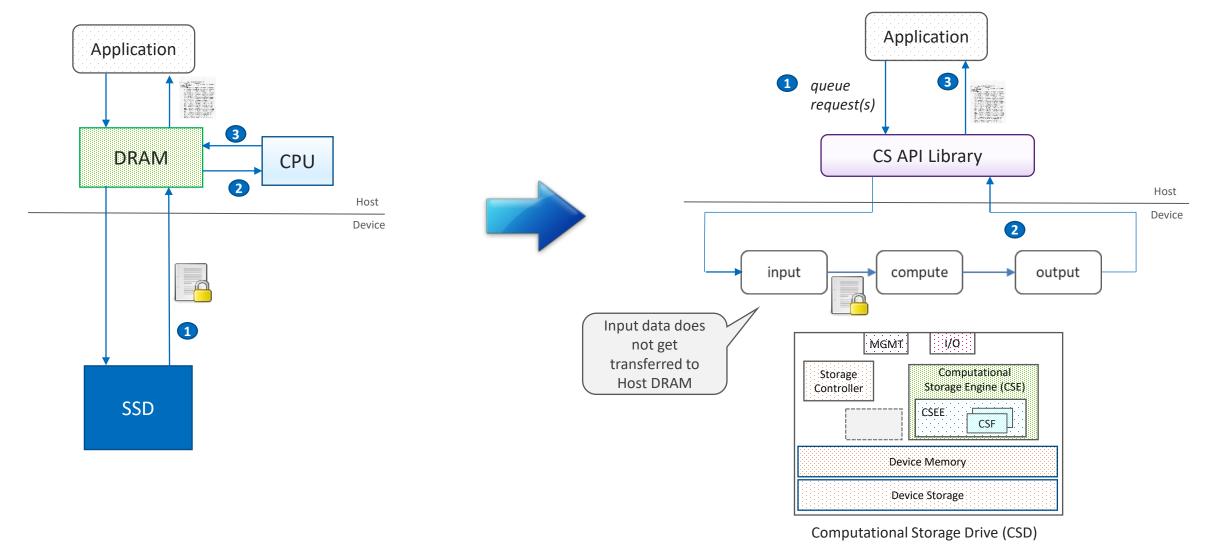
- Library may interface with additional plugins based on implementation requirements
- Plugins help connect a CSx to abstracted CS interfaces
- Extensible Interface

API Requirements

- One interface across CS devices: CSP, CSD, CSA
- Discovery
- Device Access
- Device Memory (mapped/unmapped) allocations
- Near Storage Access
- Copy Device Memory
- Download Functions (CSFs)
- Execute CSFs
- Device Configuration & Management
- Security



Applying Computational Storage



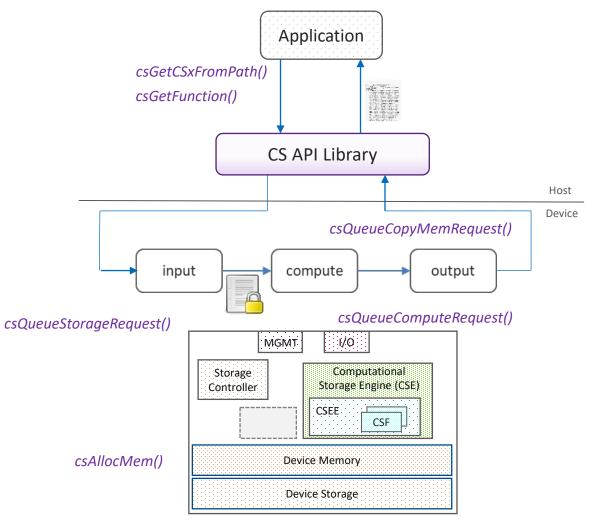
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Example with CS APIs

- 1. Discover CSx & CSF
- 2. Allocate Device Memory
- 3. Queue Storage Request
- 4. Queue Compute Request
- 5. Queue Copy Memory Request











Explore SNIA Computational Storage Activities

SNIA Computational Storage Technical Work Group

- Actively working on establishing hardware and software architectures to allow for compute to be more tightly coupled with storage at the system and drive level
- SNIA Computational Storage Special Interest Group
 - Fostering the acceptance and growth of computational storage in the marketplace
- SNIA Computational Storage Architecture and Programming Model v0.8 rev 0
 - Defines recommended behavior for hardware and software that supports Computational Storage
- SNIA Computational Storage API v0.5 rev 0
 - Defines the interface between an application and a Computational Storage device (CSx)

Is your company a SNIA member? *Find outand <u>contribute to the specifications</u>*

Implementing computational storage? **Provide a public feedback comment**



Thanks for Watching Our Webcast

- Please rate this webcast and provide us with feedback
- A link to this webcast and the PDF of the slides are posted to the SNIA Compute Memory and Storage Initiative website at

https://www.snia.org/forums/cmsi/knowledge/articlespresentations

- You can also find this webcast and many other videos and presentations on today's topics in the <u>SNIA</u> <u>Educational Library</u>
- A Q&A from this webcast will be posted to the SNIA <u>Compute, Memory, and Storage Blog</u>





What's New in Computational Storage? A Conversation with SNIA Leadership

🛗 August 28, 2021 🙎 SNIAOnStorage 🛸 Computational Storage, Standards

The latest revisions of the SNIA Computational Storage Architecture and Programming Model Version 0.8 Revision 0 and the Computational Storage API v0.5 rev 0 are now live on the SNIA website. Interested to know what has been added to the specifications, SNIAOnStorage met "virtually" with Jason Molgaard, Co-Chair of the SNIA Computational Storage Technical Work Group, and Bill Martin, Co-Chair of the SNIA Technical Council and editor of the specifications, to get the details.

Both SNIA volunteer leaders stressed that they welcome ideas about the specifications and invite industry colleagues to join them in continuing to define computational storage standards. The two



WELCOME TO THE SNIA CMSI BLOG

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The SNIA Compute, Memory, and Storage Initiative (CMSI) supports the acceptance and growth of Computational Storage, Persistent Memory, and Solid State Storage in the marketplace. All posts added to this blog are contributed by members of the CMSI. Please feel free to leave comments and ask questions on our posts. To learn more about the CMSI - visit our website.

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We welcome your questions

Thank you for watching and please rate the session

